

Notice of References Cited		Application/Control No. 09/885,761	Applicant(s)/Patent Under Reexamination KUWAZAWA, KAZUNOBU
		Examiner Stanetta D. Isaac	Art Unit 2812

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*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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B	US-4,855,247	08-1989	Ma et al.	438/305
C	US-5,153,145	10-1992	Lee et al.	438/305
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NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)		
U	Stanley Wolf and Richard N. Tauber, Silicon Processing For The VLSI Era, Lattice Press, Vol. I, pages 522-535		
V			
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A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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